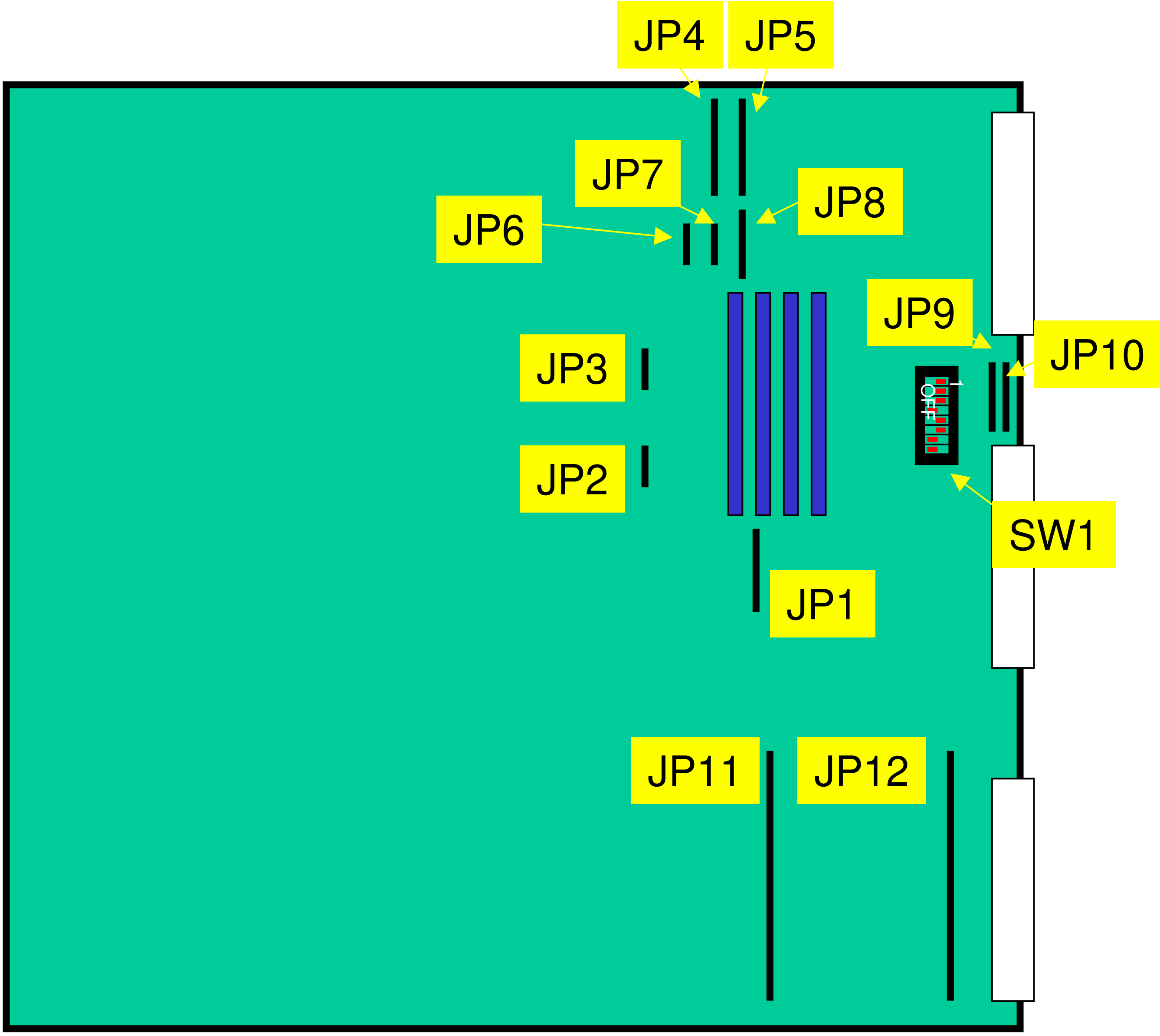
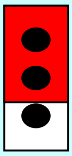
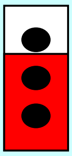
Jumper pins



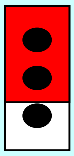

JP1,JP8: Internal use (for programing XC9572)
JP10,JP11: For future use

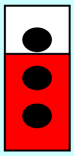
## JP2 Control clock selector

1

Backplane Clock
(Default)

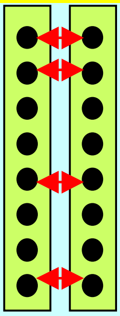
1

16MHz VME system Clock

## JP3 ADC clock selector

1

Backplane Clock
(Default)

1

Front Panel ECL Clock

## JP4 JP5

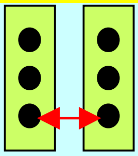
1

LSB
MSB

Example: $6C_H$

8bit Interrupt Vector:
Bits are 0,
if JP4 and JP5
are connected

(Default : All Open)

## JP6 JP7

1

LSB
MSB

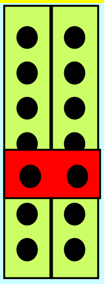
Example: $3_H$

3bit Interrupt Level
Bits are 0,
if JP6 and JP7
are connected

(Default : All Open)

## JP9 JP10

Interrupt Connection
to VME bus

1

IRQ7
IRQ1

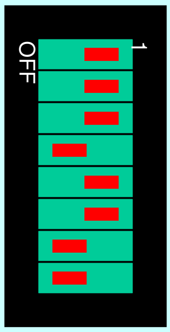
Example: $3_H$

(Default : All Open)

Don't short more than one pair.
The setup should be consistent to JP6/7.

## SW1 VME address offset

1 ◄► 0



LSB
MSB

Example: $C8_H$