

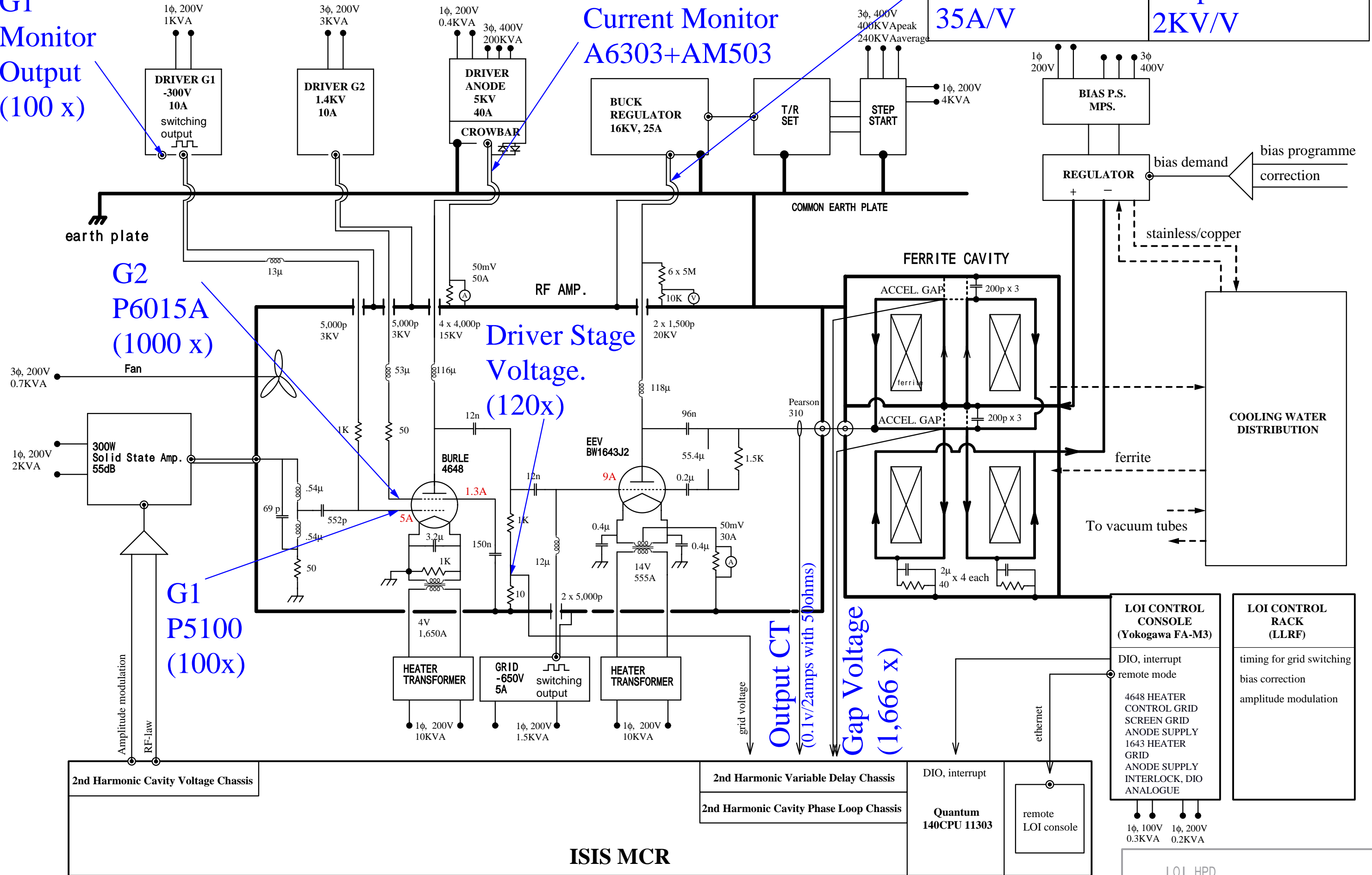
# LOI HIGH POWER DRIVE SYSTEM

G1  
Monitor  
Output  
(100 x)

Current Monitor  
A6303+AM503

J3  
Output I Monitor  
35A/V

J2  
Output V Monitor  
2KV/V



G2  
P6015A  
(1000 x)

Driver Stage  
Voltage.  
(120x)

G1  
P5100  
(100x)

Output CT  
(0.1v/2amps with 50ohms)

Gap Voltage  
(1,666 x)

LOI CONTROL CONSOLE (Yokogawa FA-M3)  
DIO, interrupt remote mode  
4648 HEATER CONTROL GRID  
SCREEN GRID  
ANODE SUPPLY  
1643 HEATER GRID  
ANODE SUPPLY  
INTERLOCK, DIO ANALOGUE  
1φ, 100V 0.3KVA 1φ, 200V 0.2KVA

LOI CONTROL RACK (LLRF)  
timing for grid switching  
bias correction  
amplitude modulation

LOI HPD  
2nd Harmonc Collaboration  
August/19/'05, Y1