

PROGRESS SUMMARY
(Hall 2 until February 23, 2007)

Y Irie
for the LOI collaboration group

After move to the Hall 2, cabling, plumbing and partitioning works for the new arrangement of LOI have been performed intensively in February, 2007. Y Irie, A Takagi (JAEA/KEK), D Horan and M Middendorf (ANL) have joined for these works. Since the present status as of February 14 has been reported in the minutes of the 10th collaboration meeting, the progress after the meeting till February 23, 2007 is described briefly in this report.

CABLING

The LOI will be developed in the Hall 2, and will be moved to the SP6 of the ISIS synchrotron for the beam test in near future (Fig.1). The breakout boxes are introduced near the HPD test area to facilitate the cable re-connections associated with the movement of the LOI system between SP6 and Hall 2. Every cable which goes to the HPD, the cavity and the water manifold is relayed at the breakout boxes, which include in the following:

Breakout Box 1: all cables except for high voltage cables (fig. 2),

Breakout Box 2: anode and screen supplies for tetrode (fig. 3),

Breakout Box 3: anode supply for triode with Ross relay for discharge (fig. 4).

As seen in the figures, all connections have been completed except for those between breakout box and anode terminals at the HPD. The power cables to feed the cavity bias current do not pass through the breakout box, but are newly laid to the cavity at the Hall 2 from the power supply which is adjacent to the cavity: the supply is also used to feed the cavity at the SP6. Schematic diagram of the cable connection is shown in fig. 5. Details are shown for the multicore cables in breakout box 1 in Table 1, the chiller cables in Table 2 and the BNC cables in Table 3.

COOLING WATER SYSTEM

The circulating cooling water at 20°C in the Hall 2 is used for cooling the triode anode, tetrode grids and the resonant resistor in the HPD via the cooling water manifold. The first flushing test was made successfully on February 21, where the input and output pressures to the

water manifold were 6.5 and 2kg/cm², respectively. The water flows were 140ℓ/min for triode anode, 30ℓ/min for resonant resistor, and 7.5ℓ/min for grids. The tetrode anode cooling will be performed with the 195kW chiller and heat exchanger system (fig. 6). The plumbing work for this system is under construction as well as the cavity and liquid resistor.

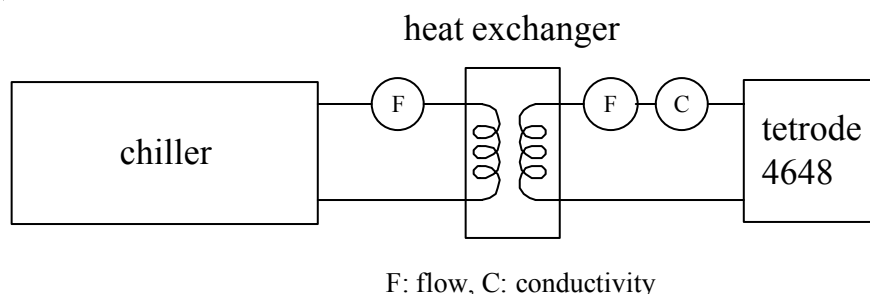


Figure 6. Cooling system for tetrode anode

Interlock signals such as chiller flow, flow and conductivity at the 2ndary circuit should be incorporated.

To-Do-List at the next LOI EXPERIMENTS

Assuming the chiller system for tetrode and the cavity bias cable will be ready, the following items should be performed before generating RF voltage.

- (1) high voltage, insulation, crowbar tests,
- (2) interlock test,
- (3) sequence test: on/off control of the power supplies.

In the RF generation test, items to be pursued are,

- (4) investigation of the waveform deformations at the driver stage,
- (5) stable high-voltage production,
- (6) cavity tuning,
- (7) cavity phasing to fundamental ones.

LOI ARRANGEMENT IN HALL 2

FEBRUARY 2007.

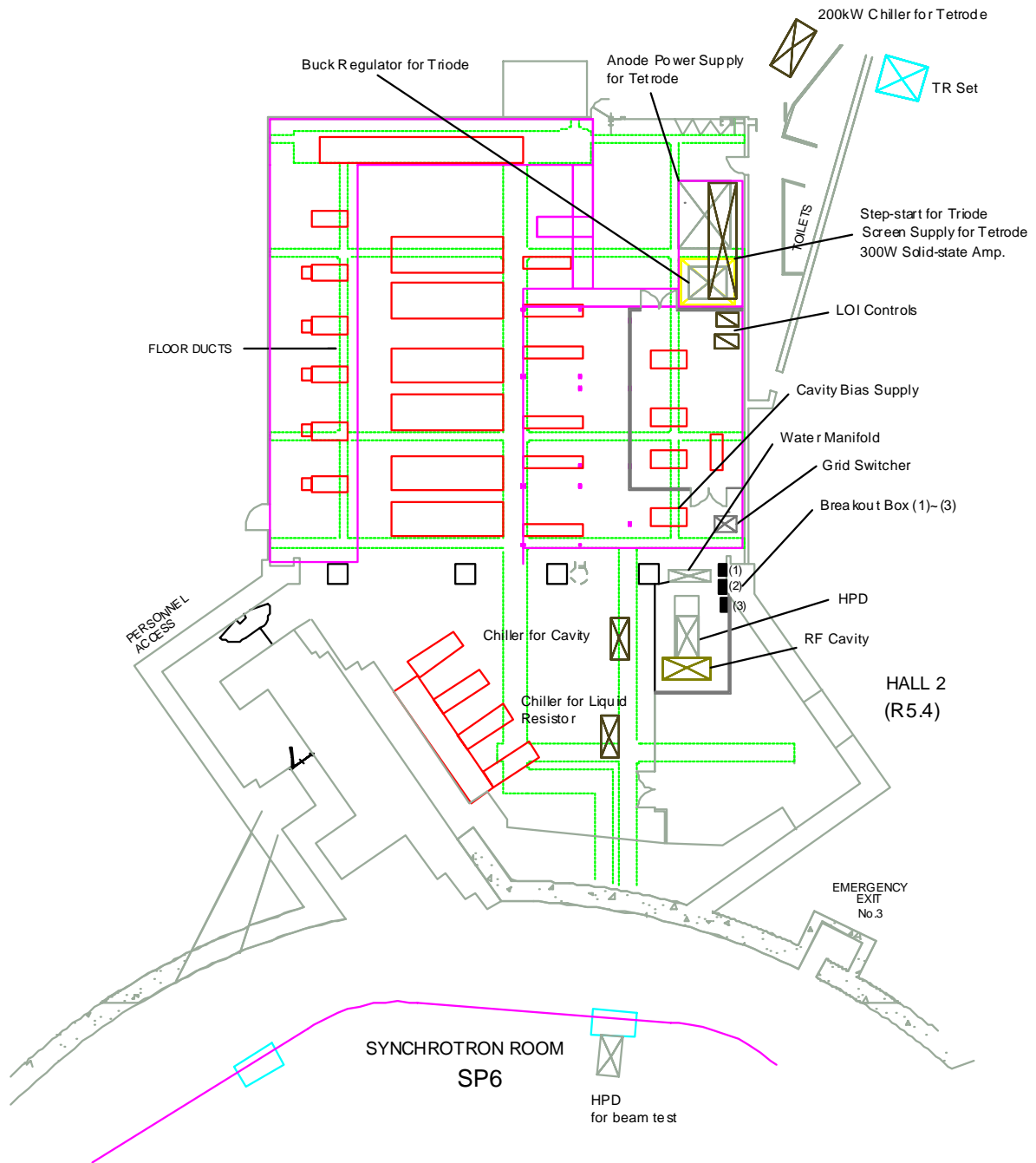


Figure 1

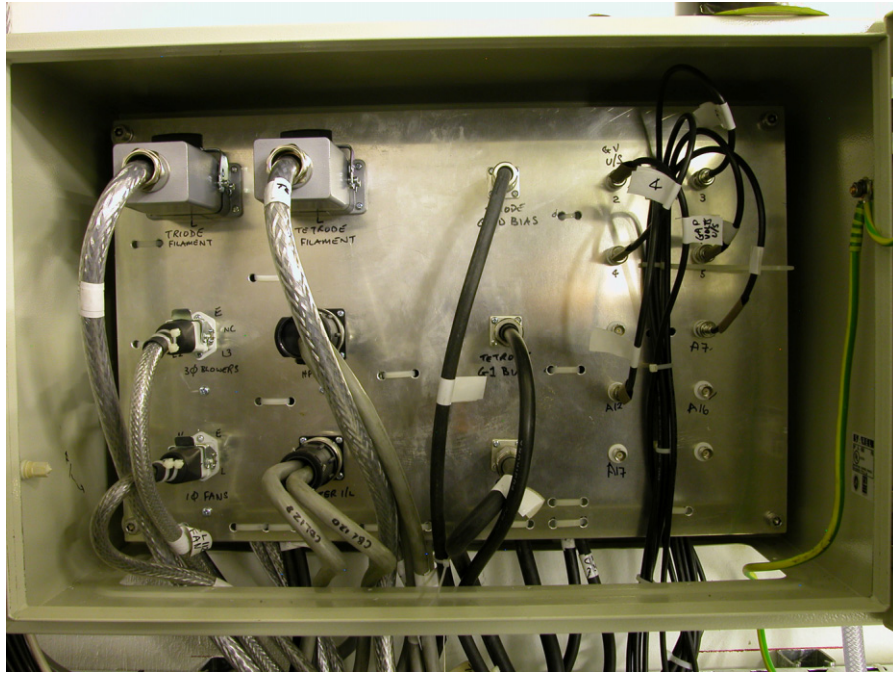


Figure 2a. Front view of the breakout box 1.

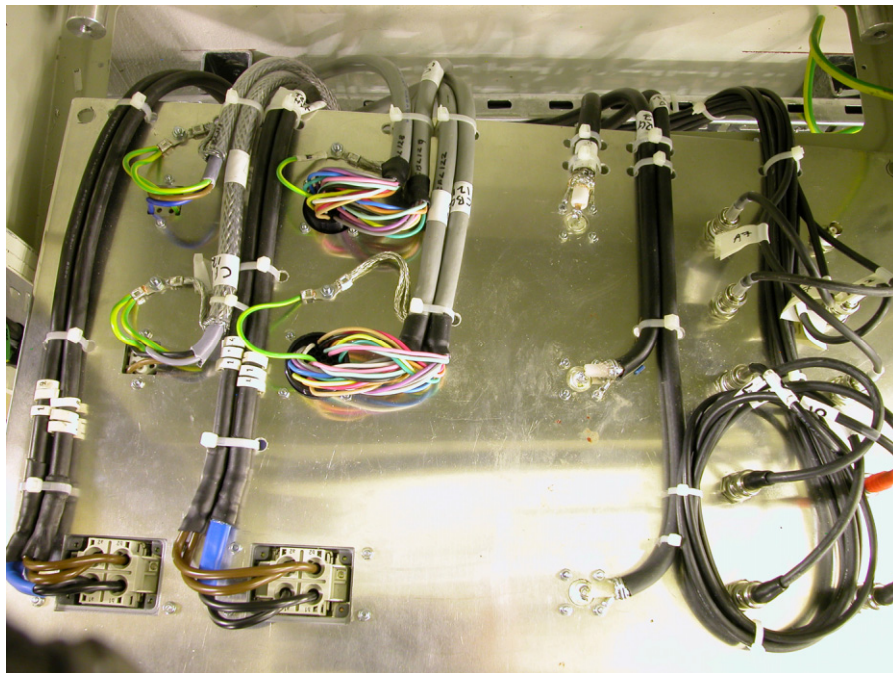


Figure 2b. Backside view of the breakout box 1.



Figure 3. Breakout box 2 for the tetrode anode and screen supplies.

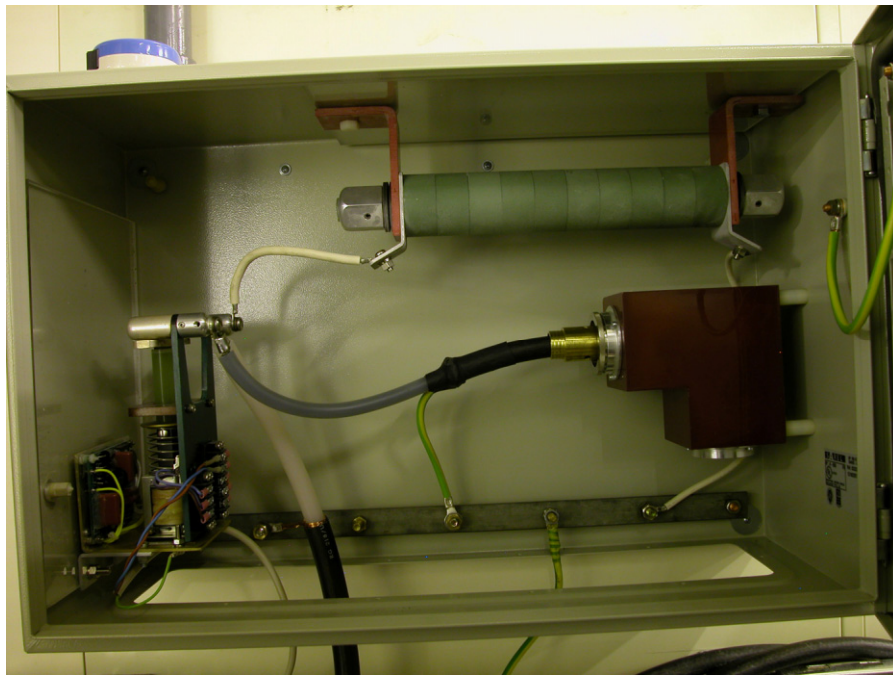
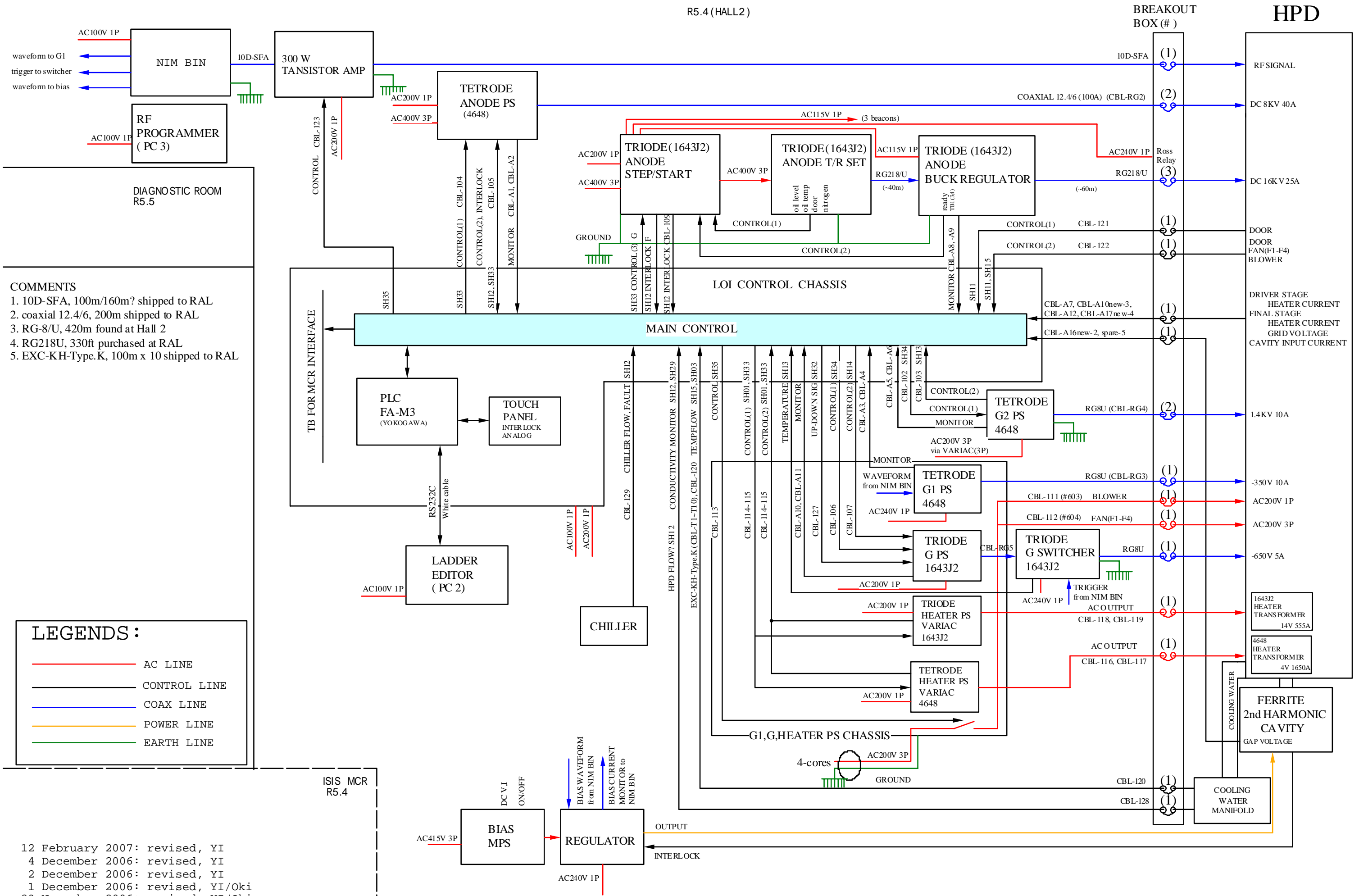


Figure 4. Breakout box 3 for the triode anode supply with Ross relay.



CABLE CONNECTIONS OF THE LOI 2nd HARMONIC RF SYSTEM

Figure 5

- 12 February 2007: revised, YI
- 4 December 2006: revised, YI
- 2 December 2006: revised, YI
- 1 December 2006: revised, YI/Oki
- 29 November 2006: revised, YI/Oki
- 6 December 2004: YI/Oki

MODULE	NAME	SIGNAL TYPE	TERMINAL NO.	CABLE NO.	BREAKOUT BOX	LOI CONTROL
Driver Anode Supply	anode voltage	analog	X1 terminal (SH No.G-14)	CBL-A1		
	anode current	analog		CBL-A2		
	anode current	analog	Tektronix A6303+AM503	CBL-A2new		
G1 Supply	G1 voltage			CBL-A3		
	G1 voltage	analog	monitor output	CBL-A3new		
	G1 current			CBL-A4		
G2 Supply	G2 voltage	analog	monitor output	CBL-A5		
	G2 current	analog	monitor output	CBL-A6		
Driver Heater	heater current	analog	HPD BNC	CBL-A7		
Final Anode Supply	anode voltage	analog	BR J2	CBL-A8		
			BR J2	CBL-A8new		
Final Anode Supply	anode current	analog	BR J3	CBL-A9		
			BR-J3	CBL-A9new		
Grid Voltage Monitor/ Upstream	voltage			CBL-A10		incorporation into MCR cavity lock system
	RF voltage	analog	HPD BNC	CBL-A10new-sp.3		
Grid Current Monitor	current			CBL-A11		
Final Heater	heater current	analog	HPD BNC	CBL-A12		
300W Amplifier				CBL-A13		
Bias Supply	voltage			CBL-A14		
	current			CBL-A15		
Gap Voltage Monitor	RF voltage	analog	Cavity Voltage Divider	CBL-A16		incorporation into MCR cavity lock system
				CBL-A16new-sp.2		
Cavity Input Current Monitor Pearson 310	RF current	analog	HPD BNC	CBL-A17		incorporation into MCR cavity lock system?
				CBL-A17new-sp.4		
Grid Switcher	TTL trigger		Grid Switcher BNC input	sp.1		
Grid Voltage Monitor/ Downstream				sp.5		
(spare)						

